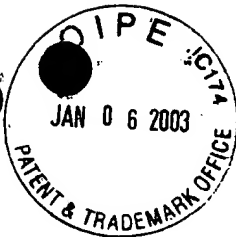


Docket No.: 57454-138



12/suppl. IOS
D/ing
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yasuhiko TSUKIKAWA

Serial No.: 09/877,027

Group Art Unit: 2811

Filed: June 11, 2001

Examiner: Linh M. NGUYEN

For: CONFIGURATION FOR GENERATING A CLOCK INCLUDING A DELAY CIRCUIT AND
METHOD THEREOF

RECEIVED
JAN - 7 2003
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

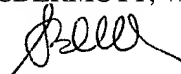
Commissioner for Patents
Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. This Information Disclosure Statement is being filed supplementary to the Information Disclosure Statement filed November 13, 2002. Please note that the prior art reference cited on the 1449 Form as Taiwanese Publication No. 090119749 should read Taiwanese Publication No. 387065, as appears on the corresponding 1449 Form. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


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